

**A DEVICE INCLUDING AN EQUALIZER AND AN AMPLIFICATION CHAIN  
FOR BROADBAND INTEGRATED CIRCUIT APPLICATIONS**

Abstract of the Invention

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A device includes an equalizer and an amplification chain in a transmitter or a receiver. The device comprises an equalizer for equalizing an input signal. The equalizer is positioned between two amplifier stages, which amplify the signal. The positioning of the equalizer greatly improves the noise figure and the distortion figures on the output signal. The device can be

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packaged into an integrated circuit for greater performance figures.